

UNITED STATES PATENT AND TRADEMARK OFFICE  
**CERTIFICATE OF CORRECTION**

PATENT NO. : 6,895,570 B2  
DATED : May 17, 2005  
INVENTOR(S) : David Lewis et al.

Page 1 of 2

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Column 12.

Line 8, please replace "inerconnects" with -- interconnects --.  
Line 17, please replace "horizontal" with -- horizontal --.  
Line 25, please replace "pogrammable" with -- programmable --.  
Line 66, please replace "pluality" with -- plurality --.

Column 13.

Line 20, please replace "onsiderations" with -- considerations --.  
Lines 32-33, please replace "wires having first logical length" with -- wires having a first logical length --.  
Lines 35-36, please replace "wires having a second logic length" with -- wires having a second logical length --.  
Line 58, please replace "length of the six subset" with -- length of the sixth subset --.

Column 14.

Line 39, please replace "haing" with -- having --.  
Line 42, please replace "rchilecture" with -- architecture --.  
Line 46, please replace "the fourth subset of ythe plurality of wires," with -- the fourth subset of the plurality of wires, --.

Column 15.

Line 8, please replace "plurality of wires to the second starting point," with -- plurality of wires at the second starting point, --.  
Line 19, please replace "pluraity" with -- plurality --.

Column 16.

Line 1, please replace "clim 23," with -- claim 23, --.  
Line 22, please replace "pllularity" with -- plurality --.  
Line 38, please replace "iniput" with -- input --.  
Line 48, please replace "of function block," with -- of function blocks, --.

Column 17.

Line 64, please replace "of claim 5," with -- of claim 35, --.

Column 18.

Line 57, please replace "a first one of the plurality of function blocks" with -- a first one of the fourth subset of the plurality of wires oriented in the second direction, and a particular one of the plurality of function blocks --.

UNITED STATES PATENT AND TRADEMARK OFFICE  
**CERTIFICATE OF CORRECTION**

PATENT NO. : 6,895,570 B2  
DATED : May 17, 2005  
INVENTOR(S) : David Lewis et al.

Page 2 of 2

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Column 19.

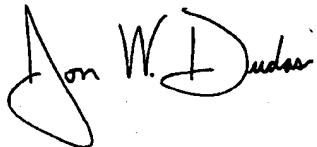
Line 6, please replace "a third one of the plurality of function blocks" with -- a third one of the second subset of the plurality of wires oriented in the second direction, and a particular one of the plurality of function blocks --.

Column 20.

Line 6, please replace "pllurality" with -- plurality --.

Signed and Sealed this

Thirtieth Day of August, 2005



---

JON W. DUDAS  
Director of the United States Patent and Trademark Office